

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/966,015
Filing Date	September 27, 2001
First Named Inventor:	Vincent J. Zimmer
Art Unit	2123
Examiner Name	Proctor, Jason S.
Attorney Docket Number	42390P11198

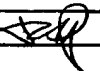
Sheet

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of

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
	1	us-	6,075,938	06-13-2000	Bugnion et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
JJP	2	WO	02/052404	A2	07-04-2002	Intel Corporation		

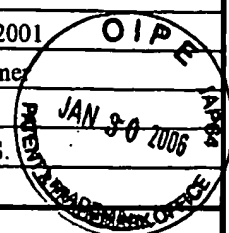
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Date Considered

4/3/06

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Sheet	2	of	2	Attorney Docket Number	42390P11198
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
JZ	3	EBICIOGLU, K. et al., "IBM Research Report - DAISY: Dynamic Compilation for 100% Architectural Compatibility," Computer Science, 08 May 1996. http://citeseer.ist.psu.edu/2006.html			
JZ	4	BRACKIN, S. H., "An Interface Specification Language for Automatically Analyzing Cryptographic Protocols," Proceedings., 1997 Symposium on San Diego, CA, USA, 10-11 February 1997, pp. 40-51.			
JZ	5	DEVANBU, P. T. et al., "Techniques for Trusted Software Engineering," Proceedings of the 1998 International Conference on Kyoto, Japan, 19-25 April 1998, pp. 126-135.			
JZ	6	EBICIOGLU, K. et al., "An Eight-Issue Tree-VLIW Processor for Dynamic Binary Translation," 16 September 1998. http://www.research.ibm.com/vliw/isca31/An%20eight-issue%20tree-VLIW%20processor%20for%20dynamic%20binary%20translation.pdf			
JZ	7	ROBIN, J. S. et al., "Analysis of the Intel Pentium's Ability to Support a Secure Virtual Machine Monitor," 11 June 2000. http://citeseer.ist.psu.edu/robin00analysis.html			



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